

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Ricardo E. Gonzalez et al.

APPLICATION NO.: 10/815,453

FILED: March 31, 2004

TITLE: Configuring a Multi-Processor System

EXAMINER: Marshall M. McLeod

GROUP ART UNIT: 2457

CONFIRMATION NO.: 7783

ATTY.DKT.NO.: PA2718US

INFORMATION DISCLOSURE STATEMENT

Under 37 C.F.R. § 1.56, and 1.97-1.98

SIR:

Pursuant to the provisions of 37 C.F.R. §§ 1.56 and 1.97-98 of the Rules of Practice in Patent Cases, enclosed herewith is form PTO-SB-08, listing several references. The Examiner is requested to make these references of official record in the application.

The references cited may be material to examination of the application and are submitted in compliance with Applicants' duty of disclosure as defined by 37 C.F.R. § 1.56.

The Applicants wish to make the Examiner aware of, and invite the Examiner to consider, as is appropriate, the following office actions from the following U.S. patent applications:

- Notice of Allowance mailed June 17, 2009 in U.S. patent application number 11/204,555, filed August 15, 2005;
- Office Action mailed December 16, 2008 in U.S. patent application number 11/204,555, filed August 15, 2005.

No representation is made or intended as to the completeness of this list, nor is the inclusion of any reference on this list an admission that it is prior art or pertinent to this application.

Applicants believe no fee is due with this submission. If a fee is due, however, the Commissioner is hereby authorized to charge any necessary fee to Account Number 06-0600.

Respectfully submitted,
Ricardo E. Gonzalez et al.

June 29, 2009

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